TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

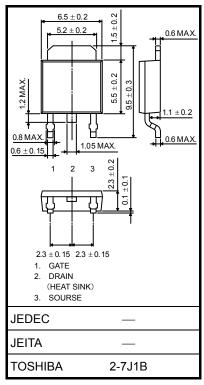
# TK2P60D

#### Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 3.3  $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 1.0 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A (V_{DS} = 600 \ V)$
- Enhancement-mode:  $V_{th} = 2.4$  to 4.4 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	۱ <sub>D</sub>	2		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	8	A	
Drain power dissipati	on (Tc = 25°C)	PD	60	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	101	mJ	
Avalanche current		I <sub>AR</sub>	2	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	6	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight : 0.36 g (typ.)

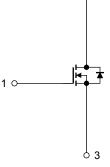
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.08	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W	

Note 1: Please use devices on conditions that the channel temperature is below 150°C. Note 2:  $V_{DD} = 90 \text{ V}, T_{ch} = 25^{\circ}\text{C}$  (initial), L = 44.1 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 2 A Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



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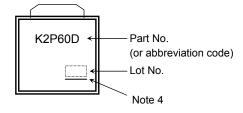
**Electrical Characteristics (Ta = 25°C)** 

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		IDSS	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source break	kdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600		_	V
Gate threshold volt	tage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.4		4.4	V
Drain-source ON-r	esistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ A}$		3.3	4.3	Ω
Forward transfer a	dmittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ A}$	0.3	1.0	—	S
Input capacitance		C <sub>iss</sub>			280	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz		1.5	_	
Output capacitance		C <sub>oss</sub>			30	_	
Switching time	Rise time	tr	$V_{GS} = 1 \text{ A Vout}$ $V_{GS} = 0 \text{ V}$ $C = 0 \text{ V}$ $R_L = 200 \Omega$ $C = 0 \text{ V}$	_	15	_	
	Turn-on time	t <sub>on</sub>			35		ns
	Fall time	t <sub>f</sub>			7		
	Turn-off time	t <sub>off</sub>			55	_	
Total gate charge		Qg			7		nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD}\approx 400~V,~V_{GS}=10~V,~I_{D}=2~A$		4		
Gate-drain charge		Q <sub>gd</sub>	]	—	3		

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	2	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	8	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 2 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 2 \text{ A}, V_{GS} = 0 \text{ V},$	_	550	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	2.2	_	μC

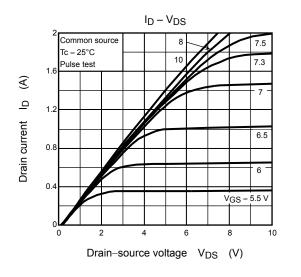
## Marking

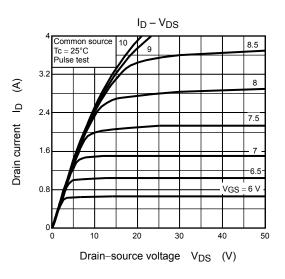


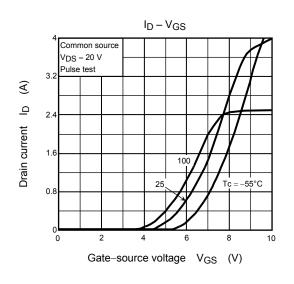
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

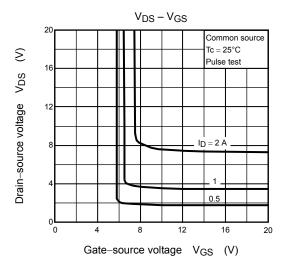
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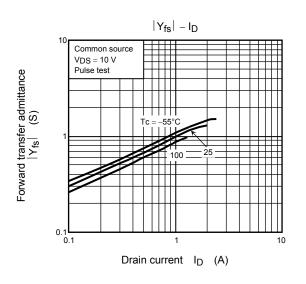
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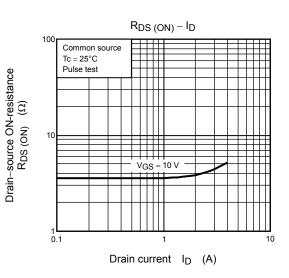




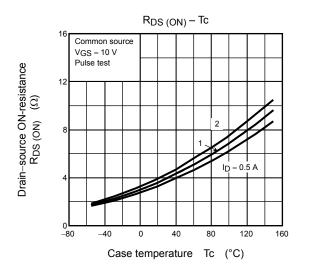


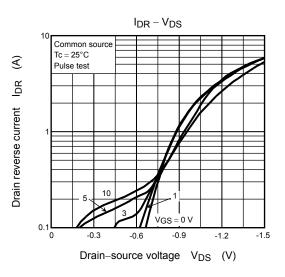


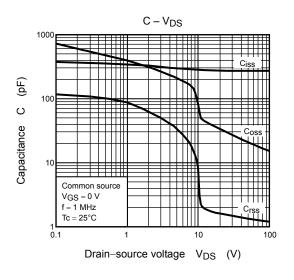


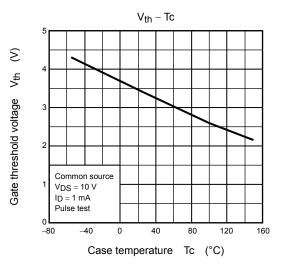


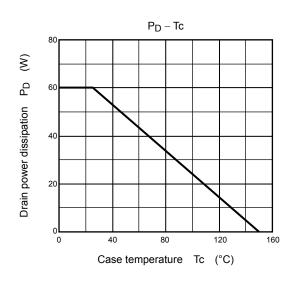
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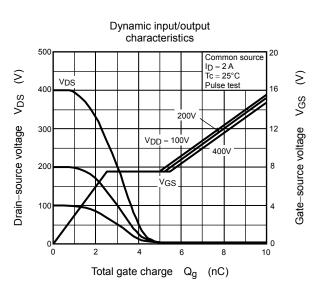


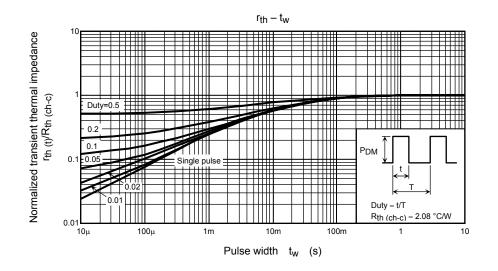


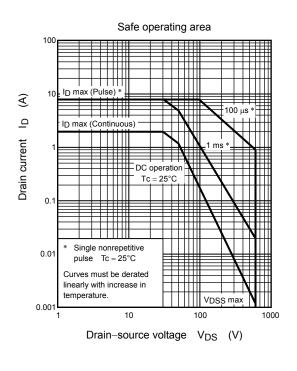


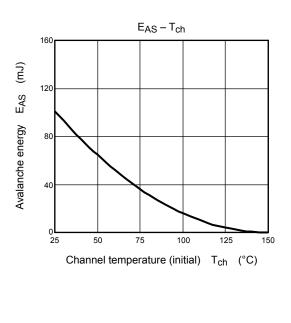


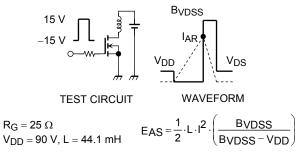












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